

**Xilinx**

## XCZU7EV-2FBVB900I

Karolo ea Karolo:	XCZU7EV-2FBVB900I
Moetsi / Mochini:	Xilinx
Tlhaloso ea Sehlahisoa	IC FPGA 204 I/O 900FCBGA
	<a href="#">1.XCZU7EV-2FBVB900I.pdf</a>
	<a href="#">2.XCZU7EV-2FBVB900I.pdf</a>
	<a href="#">3.XCZU7EV-2FBVB900I.pdf</a>
	<a href="#">4.XCZU7EV-2FBVB900I.pdf</a>
Datasheets:	
Boemo ba RoHS	Tataiso ea mahala / RoHS e lumellanang
Sekepe se Tsoang ho	Hong Kong
Tsela ea Shipment	DHL/Fedex/TNT/UPS/EMS

**KOPO EA HO**  
**QOTSA**

Setšoantšo e kanna ea ba moemeli. Bona litlhaloso bakeng sa lintlha tsa sehlahisoa.

### Litlhaloso tsa XCZU7EV-2FBVB900I

KAROLO EA KAROLO	XCZU7EV-2FBVB900I
MOETSI	Xilinx
TLHALOSO	IC FPGA 204 I/O 900FCBGA
TEMO EA MAHALA EA BOEMO BO PHAHAMENG / BOEMO BA ROHS	Tataiso ea mahala / RoHS e lumellanang
	<a href="#">1.XCZU7EV-2FBVB900I.pdf</a>
	<a href="#">2.XCZU7EV-2FBVB900I.pdf</a>
	<a href="#">3.XCZU7EV-2FBVB900I.pdf</a>
	<a href="#">4.XCZU7EV-2FBVB900I.pdf</a>
LEQEPHE LA LINTLHA	
PACKAGE EA LISEBELISOA TSA THEPA	900-FCBGA (31x31)
LEBELO	533MHz, 600MHz, 1.3GHz
SEHLOOHO	Zynq® UltraScale+™ MPSoC EV
RAM SIZE	256KB
LITŠOBOTSI TSA MANTLHA	Zynq®UltraScale+™ FPGA, 504K+ Logic Cells
LI-PÉRIIPHERALS	DMA, WDT
PACKAGING	Tray
PACKAGE / CASE	900-BBGA, FCBGA
MOCHESO O SEBETSANG	-40°C ~ 100°C (TJ)
PALO EA I / O	204
MOKHOA OA HO UTLOISISA BOHLOKO (MSL)	4 (72 Hours)
NAKO EA MOETA-PELE E TLOAELEHILENG	10 Weeks
TEMO EA MAHALA EA BOEMO BO PHAHAMENG / BOEMO BA ROHS	Lead free / RoHS Compliant
BOIMA BA FLASH	-
TLHALOSO E QAQILENG	Quad ARM® Cortex®-A53 MPCore™ with CoreSight™, Dual ARM®Cortex™-R5 with CoreSight™, ARM Mali™-400 MP2 System On Chip (SOC) IC Zynq® UltraScale+™ MPSoC EV Zynq®UltraScale+™ FPGA, 504K+ Logic Cells 256KB 533MHz, 600MHz, 1.3GHz 900-FCBGA (31x31)
CORE PROCESSOR	Quad ARM® Cortex®-A53 MPCore™ with CoreSight™, Dual ARM®Cortex™-R5 with CoreSight™, ARM Mali™-400 MP2
HO BUISANA	CANbus, EBI/EMI, Ethernet, I²C, MMC/SD/SDIO, SPI, UART/USART, USB OTG
LITLHÖLISANO	MCU, FPGA

### Metsu e amanang

Xilinx XCZU7EV-2FBVB900I	Morekisi oa XCZU7EV-2FBVB900I	Mofani oa XCZU7EV-2FBVB900I
Theko ea XCZU7EV-2FBVB900I	XCZU7EV-2FBVB900I Litšoantšo	Setšoantšo sa XCZU7EV-2FBVB900I
Datasheet ea PDF ea XCZU7EV-2FBVB900I	XCZU7EV-2FBVB900I Download Datasheet	Datasheet ea XCZU7EV-2FBVB900I
XCZU7EV-2FBVB900I Stock	Reka XCZU7EV-2FBVB900I	Reka Xilinx XCZU7EV-2FBVB900I
Xilinx XCZU7EV-2FBVB900I	Mofani oa Xilinx	Morekisi oa Xilinx
Xilinx XCZU7EV-2FBVB900I	Xilinx Inc. XCZU7EV-2FBVB900I	

### Lihlahisoa tse amanang

<p><b>XCZU7EV-2FFVF1517E</b>                  Bahlahisi: Xilinx                  Tlhaloso: IC FPGA 464 I/O 1517FCBGA                  E teng thepeng e teng ka lebenkeleng: <b>Out stock</b></p> <p style="text-align: center; background-color: #0056b3; color: white; padding: 5px; border-radius: 5px;"><b>RFQ</b></p>	<p><b>XCZU7EV-2FFVC1156E</b>                  Bahlahisi: Xilinx                  Tlhaloso: IC FPGA 360 I/O 1156FCBGA                  E teng thepeng e teng ka lebenkeleng: <b>Out stock</b></p> <p style="text-align: center; background-color: #0056b3; color: white; padding: 5px; border-radius: 5px;"><b>RFQ</b></p>
<p><b>XCZU7EV-1FFVC1156E</b>                  Bahlahisi: Xilinx                  Tlhaloso: IC FPGA 360 I/O 1156FCBGA                  E teng thepeng e teng ka lebenkeleng: <b>Out stock</b></p> <p style="text-align: center; background-color: #0056b3; color: white; padding: 5px; border-radius: 5px;"><b>RFQ</b></p>	<p><b>XCZU7EV-2FBVB900E</b>                  Bahlahisi: Xilinx                  Tlhaloso: IC FPGA 204 I/O 900FCBGA                  E teng thepeng e teng ka lebenkeleng: <b>Out stock</b></p> <p style="text-align: center; background-color: #0056b3; color: white; padding: 5px; border-radius: 5px;"><b>RFQ</b></p>
<p><b>XCZU7EV-1FFVF1517E</b>                  Bahlahisi: Xilinx                  Tlhaloso: IC FPGA 464 I/O 1517FCBGA                  E teng thepeng e teng ka lebenkeleng: <b>Out stock</b></p> <p style="text-align: center; background-color: #0056b3; color: white; padding: 5px; border-radius: 5px;"><b>RFQ</b></p>	<p><b>XCZU7EV-2FFVF1517I</b>                  Bahlahisi: Xilinx                  Tlhaloso: IC FPGA 464 I/O 1517FCBGA                  E teng thepeng e teng ka lebenkeleng: <b>Out stock</b></p> <p style="text-align: center; background-color: #0056b3; color: white; padding: 5px; border-radius: 5px;"><b>RFQ</b></p>
<p><b>XCZU7EV-3FBVB900E</b>                  Bahlahisi: Xilinx                  Tlhaloso: IC FPGA 204 I/O 900FCBGA                  E teng thepeng e teng ka lebenkeleng: <b>Out stock</b></p> <p style="text-align: center; background-color: #0056b3; color: white; padding: 5px; border-radius: 5px;"><b>RFQ</b></p>	<p><b>XCZU7EV-1FBVB900I</b>                  Bahlahisi: Xilinx                  Tlhaloso: IC FPGA 204 I/O 900FCBGA                  E teng thepeng e teng ka lebenkeleng: <b>Out stock</b></p> <p style="text-align: center; background-color: #0056b3; color: white; padding: 5px; border-radius: 5px;"><b>RFQ</b></p>
<p><b>XCZU7EV-2FFVC1156I</b>                  Bahlahisi: Xilinx                  Tlhaloso: IC FPGA 360 I/O 1156FCBGA                  E teng thepeng e teng ka lebenkeleng: <b>Out stock</b></p> <p style="text-align: center; background-color: #0056b3; color: white; padding: 5px; border-radius: 5px;"><b>RFQ</b></p>	<p><b>XCZU7EV-3FFVC1156E</b>                  Bahlahisi: Xilinx                  Tlhaloso: IC FPGA 360 I/O 1156FCBGA                  E teng thepeng e teng ka lebenkeleng: <b>Out stock</b></p> <p style="text-align: center; background-color: #0056b3; color: white; padding: 5px; border-radius: 5px;"><b>RFQ</b></p>
<p><b>XCZU7EV-1FFVF1517I</b>                  Bahlahisi: Xilinx                  Tlhaloso: IC FPGA 464 I/O 1517FCBGA                  E teng thepeng e teng ka lebenkeleng: <b>Out stock</b></p> <p style="text-align: center; background-color: #0056b3; color: white; padding: 5px; border-radius: 5px;"><b>RFQ</b></p>	<p><b>XCZU7EV-1FFVC1156I</b>                  Bahlahisi: Xilinx                  Tlhaloso: IC FPGA 360 I/O 1156FCBGA                  E teng thepeng e teng ka lebenkeleng: <b>Out stock</b></p> <p style="text-align: center; background-color: #0056b3; color: white; padding: 5px; border-radius: 5px;"><b>RFQ</b></p>